University of Macau

Faculty of Science and Technology

Department of Electrical and Electronics Engineering

Part A: Course Outline

Course Title:	Analog Integrated Circuit Design		
Course Code:	ELEC371	Year of Study:	3
Course Mode:	Special		
Compulsory/Elective:	Elective		
Course Prerequisites:	none		
Prerequisite Knowledge	Circuit analysis, basic MOSFET operation and design principles		
Class/Laboratory Schedule:	2-hour lecturer, and 2-hour tutorial and/or laboratory per week		
Duration	One semester	Credit Units	3
Text Books and References:	Behzad Razavi, Fundamentals of Microelectronics, 1st Edition, John Wiley & Sons, Inc., 2008 [ISBN: 978-0-471-47846-1] Behzad Razavi, Design of Analog CMOS Integrated Circuits, *McGraw Hill, 2001* [ISBN: 0-07-118839-8]		
Course Description:	This course is designed to introduce Analog Integrated Circuit (IC) design fundamentals with emphasis on the current deep submicron and nanometer CMOS technologies. The course content includes single and multiple-transistor amplifiers, current mirrors, current/voltage reference, CMOS operational amplifier and its stability analysis, noise analysis, switched-capacitor and high-frequency circuits. The layout of the individual circuit blocks will be also introduced in parallel with the main course outline, such that the students can have a good understanding of the physical implementation of analog ICs. Students will gain the basic understanding of analog IC design as well as familiar with the necessary IC design and simulation tools (e.g., CADENCE).		
Topics Covered	Transistor operation modes (cutoff, subthreshold, triode, saturation) Single-transistor amplifiers and their I/O impedance and gain, Multi-transistor amplifiers and their gain, bandwidth, stability, etc., OpAmp-based building blocks: filter, inverting/non-inverting Amplifiers, integrated components (resistor, inductor and capacitor), Design of Single-Stage and Two-Stage OpAmps, Layout of Integrated Circuits, Circuit Simulations.		
Course Objectives:	1. To educate students fundamental analog circuits design skills [a, e, k] 2. To introduce students the basic analog electronics utilized in the industry and how to build up a complex system from basis [a, e, k] 3. To offer students experience on designing and simulating analog circuits in computer [a, b, c, d, e, k]		
Course Assessment:	Quiz :25%		

<table>
<tr><td></td><td>Simulation homework: 20%
Mid-term Exam. : 25%
Final Project: 30%</td></tr>
<tr><td>Relationship to Program Objectives and Outcomes</td><td>This course primarily contributes to EEE program outcomes that develop students abilities to:
a. Ability to apply knowledge of mathematics, science and engineering.
e. Ability to identify, formulate and solve engineering problems.
k. Ability to use the techniques, skills and modern engineering tools necessary for engineering practice.

This course secondarily contributes to EEE program outcomes that develop students abilities to:
b. Ability to design and conduct simulations and experiments.
c. Ability to design a system, component or process to meet desired needs.
d. Ability to function on multidisciplinary teams.</td></tr>
<tr><td>Course Contents and Relationship to Program Criteria:</td><td>
<table>
<tr><th>Week no.</th><th>Topics</th><th>Program Criteria</th></tr>
<tr><td>2</td><td>Introduction
The history and state-of-the-art analog circuits in wide type of electronics industry, the fundamental methods and challenges of systematic circuit design in CMOS technologies</td><td>ES</td></tr>
<tr><td>1</td><td>Basic MOSFET Principles and Single-Stage Amplifiers
Input/output impedance, gain, bandwidth and stability of common-source amplifier, common-gate amplifier and source follower.</td><td>ES</td></tr>
<tr><td>2</td><td>Feeback and OpAmp-Based Building Blocks
Active-resistor-capacitor Filter, Sallen-Key filter, inverting amplifier and non-inverting amplifier, filter design with different approximation such as Butterworth.</td><td>ES, CS, DE</td></tr>
<tr><td>2</td><td>Integrated Components
Basic concepts and selection criteria of integrated resistors such as polysilicon, integrated capacitors such as Metal-Over-Metal and Metal-Insulator-Metal capacitors, inductors such as bondwire and spiral</td><td>ES</td></tr>
<tr><td>1</td><td>The principle of Integrated Circuit Simulation
Brief introduction in the theories on circuit simulations, including DC, AC and Transient analysis, convergence criteria, etc.</td><td>ES, DIC, DE</td></tr>
<tr><td>1</td><td>Tutorial on Integrated Circuit Design Software
To provide one week tutorial to the Professional Integrated Circuit Design Software – Cadence, which is widely used in industry. The simulation projects are also based on Cadence.</td><td>ES, CS</td></tr>
<tr><td>2</td><td>Layout Design of Integrated Circuits
Introduce how the integrated circuits formed in CMOS, the layout of transistors, capacitors and resistors, and also important analog layout techniques.</td><td>ES</td></tr>
<tr><td>3</td><td>Single-Stage and Two-Stage OpAmps
The single-stage amplifiers – Telescopic, Folded-Cascode, Mirror; The two-stage amplifiers, Frequency Compensation, Common-Mode Feedback, Slew-Rate.</td><td>ES, DIC, CV</td></tr>
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Contribution of Course to meet the professional component:	This course educates students with basic analog circuits design skills related with wide types of CMOS electronics. Students should be able to design apposite circuits for different applications and apply knowledge of mathematics and engineering, identify formulas, build up simulation netlists to simulate electronic circuits, as well as the transferring the circuits into real integrated circuits layout
Course Instructor(s):	Dr. Pui-In Mak and Dr. Sai-Weng sin
Prepared by:	Dr. Pui-In Mak and Dr. Sai-Weng sin

Part B: General Course Information and Policies

Instructor: Dr. Pui-In Mak and Dr. Sai-Weng Sin Office: JLG211B, JLG212D

Office Hour:
Dr. Mak: Thursday 3:30~5:30 p.m. or by appointment Phone: 8794
E-mail: pimak@umac.mo

Dr. Sin: Friday 3:30~5:30 p.m. p.m. or by appointment Phone: 8795
E-mail: terryssw@umac.mo

Programme Educational Objectives

1. **Problem Solving:** Graduates have the ability to think in a critical and evaluative manner and to consider a broad perspective, in order to solve technical and nontechnical problems.
2. **Leadership and Communication:** Graduates will provide effective leadership, act in an ethical manner and skills will include the ability to communicate well and to work successfully within diverse groups.
3. **Market Acceptance:** Graduates will have successful careers in the academic environment, industrial and government organizations.
4. **Technical Competence:** Graduates will be technically competent and have a thorough grounding in the fundamentals of math and science in electrical and computer engineering and experience in engineering design. They will be able to use modern engineering techniques, skills, and tools to fulfill societal needs.

Scale: 1 (Highest) to 4 (Lowest)

	Problem Solving	**Leadership and Communication**	**Market Acceptance**	**Technical Competence**
Measurement And Instrumentation II	1	2	2	2

Remark:

- Objective for “Problem Solving” can be achieved by assignments, laboratories, mid-term exam, and projects.
- Objective for “Leadership and Communication” can be achieved by course project, report writing and presentation.
- Objective for “Market Acceptance” can be achieved by the course contents

that are required in industries.

- Objective for “Technical Competence” can be achieved by using fundamentals of math and science in electrical and computer engineering and experience in engineering project design and computer simulation.

Program Criteria Policy:

Course VS Program Criteria

Scale: 1 (Highest) to 4 (Lowest)

Course	PS	DIC	BS	CS	ES	DE	LA	CV	DM
Analog Integrated Circuit Design		2		3	1	3		4	

Terms:

Probability and Statistics (PS), Differential and Integral Calculus (DIC), Basic Science (BS), Computer Science (CS), Engineering Science (ES), Differential Equation (DE), Linear Algebra (LA), Complex Variables (CV), Discrete Mathematics (DM)

Program Outcome Policy:

Course VS Course Outcomes

(H= Highly Related, S = Supportive, N = None)

Course	a	b	c	d	e	f	g	h	i	j	k	l
Analog Integrated Circuit Design	H	S	S	S	H	N	N	S	N	N	H	H

The electrical and electronics engineering program outcomes are:

a. Ability to apply knowledge of mathematics, science and engineering.

b. Ability to design and conduct experiments.

c. Ability to design a system, component or process to meet desired needs.

d. Ability to function on multidisciplinary teams.

e. Ability to identify, formulate and solve engineering problems.

f. Understanding of professional and ethical responsibility.

g. Ability to communicate effectively.

h. Broad education necessary to understand the impact of engineering solutions in global and societal context.

i. Recognition of the need for and an ability to engage in life-long learning.

j. Knowledge of contemporary issues.

k. Ability to use the techniques, skills and modern engineering tools necessary for engineering practice.

l. Ability to use the computer/IT tools relevant to the discipline along with an understanding of their processes and limitations

Curriculum Detail

ELEC 371 Analog Integrated Circuit Design

Timetabled work in hours per week			No of teaching weeks	Total hours	No /Duration of exam papers	Max marks available from:	
Lecturer	Tutor	Practice				Exams	Course
2	0	2	14	56	1/2 hours	25	75

Term: 6th

Hours			Percentage content of					
Lecturer	Lab/tut	Other	Maths	Basic Science	Engineering Science	Engineering Design and Synthesis	Complementary Studies	Computer Studies
28	24/4	0	20	0	40	35	0	5

Design Elements

% of Design Content	Design Content in Course Work	Design Project	Design Content in Laboratories
35%	X	X	X

Course Assessment Policy:

- Homework assignments will be given to students according to the course progress, no late homework is accepted. Zero mark will be given when homework is copied.
- 3-4 Quizzes will be held during the semester.
- 1-2 simulation laboratories will be performed during the semester. 2-3 students form one group and group report should be handed up.
- 1 mid-term exam with 2 hours and 1 final project.